

ABSTRACT OF THE DISCLOSURE

Methods of optimizing a plurality of numerically controlled delay lines (NCDLs) in a DDR memory controller are presented herein. In one embodiment, a method may comprise, for example, one or more of the following: acquiring a plurality of statistics, the plurality of statistics defining an operating region for the DDR memory controller; and calculating optimal values for the plurality of NCDLs, the optimal values calculated using the plurality of statistics.